

10/511712

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
GENDRIER ET AL.)
Serial No. **NOT YET ASSIGNED**)
Filing Date: **HEREWITH**)
For: **ELECTRICALLY ERASABLE AND**)
PROGRAMMABLE, NON-VOLATILE)
SEMICONDUCTOR MEMORY DEVICE)
HAVING A SINGLE LAYER OF GATE)
MATERIAL, AND CORRESPONDING)
MEMORY PLANE)

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to the calculation of fees and examination of the present application, please enter the amendments and consider the remarks set out below.